

Patent No. 6,979,602
Application No. 10/730,755

PATENT
Atty Docket No. C016-D1

IN THE UNITED STATES PATENT OFFICE

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Applicants: Andrew Horch et al.

Assignee: T-RAM Semiconductor, Inc. (formerly T-RAM, Inc.)

Title: Method for making a recessed thyristor control port

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Issue Date: 12/27/2005

App. No.: 10/730,755

Filing Date: 12/08/2003

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TRANSMITTAL LETTER

Dear Sir:

In connection with the above-referenced matter, transmitted herewith are the following:

1. Statement of Loss of Status as Small Entity and Payment of Deficiency of Maintenance Fees Under 37 C.F.R. §1.28(c).
2. Check No. 744 for \$620 for payment of the deficiency and associated surcharge.

Please date-stamp the enclosed post card and return same to the undersigned in acknowledgement of receipt of all transmitted materials.

Respectfully submitted,



Wesley Eric Webostad
Attorney for Assignee
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I hereby certify that this correspondence is being filed via U.S. Mail, First Class, with the United States Patent and Trademark Office on 7/22/2010.

By: 
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